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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/630,778	07/31/2003	Fong Shi	60000500.1012 7891	
7	7590 06/30/2004	EXAMINER		
Jean C. Edwa		CAO, PHAT X		
SONNENSCH	EIN NATH & ROSEN			
P.O. Box 0610	80	ART UNIT	PAPER NUMBER	
Wacker Drive	Station	2814		
Chicago, IL	60606-1080	DATE MAILED: 06/30/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

<u> </u>		Application	on No.	Applicant(s)				
Office Action Summary		10/630,77		SHI, FONG				
		Examiner		Art Unit	*			
		Phat X. Ca	ao (2814				
Th MAILING DATE of this communication appears on the cover she t with the correspond nce address Period for Reply								
A SH THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR RE MAILING DATE OF THIS COMMUNICATIOnsions of time may be available under the provisions of 37 CFF SIX (6) MONTHS from the mailing date of this communication period for reply specified above is less than thirty (30) days, a period for reply is specified above, the maximum statutory perior to reply within the set or extended period for reply will, by streply received by the Office later than three months after the med patent term adjustment. See 37 CFR 1.704(b).	NN. R 1.136(a). In no even reply within the staturiod will apply and wi atute, cause the apply	ent, however, may a reply be tim story minimum of thirty (30) days Il expire SIX (6) MONTHS from to ication to become ABANDONE	nely filed s will be considered time the mailing date of this o O (35 U.S.C. § 133).				
Status								
1)🛛	Responsive to communication(s) filed on 2	9 April 2004.						
2a)□								
3)□	, —							
Disposit	ion of Claims							
_	 ✓ Claim(s) 1-5 and 7-18 is/are rejected. ✓ Claim(s) 6 is/are objected to. 							
Applicat	ion Papers							
10)	The specification is objected to by the Exame The drawing(s) filed on is/are: a) applicant may not request that any objection to Replacement drawing sheet(s) including the core The oath or declaration is objected to by the	accepted or b) the drawing(s) b rection is require	e held in abeyance. See ed if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 C				
Priority ι	ınder 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.								
Attachmen 1) Notice	e of References Cited (PTO-892)		4) Interview Summary					
3) 🛭 Infori	e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB r No(s)/Mail Date <u>7/31/03</u> .		Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:		O-152)			

DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of Group I (claims 1-18) in the reply filed on 4/29/04 is acknowledged.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 15-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lai et al (US. 6,462,405) in view of Maley (US. 6,249,136).

Lai (Fig. 2) discloses a hermetic device comprising: a substrate 40; an electronic package 41 disposed on the substrate; a sealant 43 disposed on the electronic package; and solders 42 attached to seal the electronic package 41 to the substrate 40.

Lai does not disclose a Backside Interconnect extending through the electronic package 41 from the top to the bottom surfaces.

However, Maley (Fig. 7) teaches an electronic package having a Backside Interconnect 112 extending from the top to the bottom surfaces. Accordingly, it would have been obvious to form the Backside Interconnect extending through the electronic package 41 of Lai from the top to the bottom surfaces because as taught by Maley, such Backside Interconnect is commonly used and well known for providing the

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connection to the package substrate from the circuitry formed on the top surface of the electronic package (column 3, lines 54-67 through column 4, lines 1-7).

4. Claims 1-4, 7, 9-14, 17 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lai et al in view of Maley and Heckaman et al (US. 5,023,624).

Regarding claims 1-4, 12-14 and 17-18, as discussed in details above, the combination of Lai and Maley substantially reads on the above claims, including a conformal coating 35 disposed on the sealant 43 and a cover 34 disposed on the conformal coating 35. The above combination does not disclose the electronic package is MMIC.

However, Heckaman (Fig. 1) teaches a hermetic device comprising an electronic package of MMIC or Phased Array Antenna (PAA) disposed on the substrate 20 (column 8, lines 40-50). Accordingly, it would have been obvious to form the electronic package of Lai as MMIC or PAA because it is an intended use depending upon the application which is desired for the package device, as taught by Heckaman (column 8, lines 40-50).

Regarding claim 7, Heckaman further teaches the forming of a high performance support substrate (e.g. Ga As) for high frequency applications (column 1, lines 19-21).

Regarding claims 9-10, Lai's Fig. 2 further discloses the solder attachment 42 along a periphery of the electronic package 41, to seal the electronic package 41 to the substrate 40.

Regarding claim 11, it would have been obvious to form the solder attachment 42 made of AuSn because AuSn is a well-known solder material which has a low melting point.

5. Claims 5 and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lai et al as and Maley applied to claim 1 above, and further in view of Patel (US. 5,396,403).

Regarding claim 5, Lai discloses the thermally conductive material 43 made of a metallic material (column 7, lines 7-14), but not a silicon carbide material.

However, Patel (Fig. 1) teaches a hermetic device having a sealant 19 of thermally conductive material formed on an electronic package 13 and made of a metallic material or silicon carbide material (column 4, lines 64-68). Accordingly, it would have been obvious to form the thermally conductive material of Lai with either metallic or silicon carbide because as taught by Patel, both of these materials would have a relatively low coefficient of expansion (column 4, lines 64-68).

Regarding claim 8, Patel further teaches that the thickness of silicon carbide thermally conductive layer is not critical and may adjusted as desired according to such factors as the size of the electronic package and the thermal conductivities of the specific materials being used (column 5, lines 13-17).

Allowable Subject Matter

6. Claim 6 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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The prior art fails to disclose the SiC disposed over BCB interlayer dielectric.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phat X. Cao whose telephone number is (571) 272-1703. The examiner can normally be reached on Monday - Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PC

June 25, 2004

PHAT X. CAO

PRIMARY EXAMINER

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